

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Gartstein et al.

Group No.:

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Examiner:

N/A

Filed:

December 4, 2001

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ELECTRONIC CIRCUITY

I hereby certify that his paper is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231,

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Attorney

Docket No.:

7922-84469

February 26, 200

Date

Signature

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, applicants submit the following statement consisting of:

A list of patents and other publications (and attached Form PTO-1449). I.

Π. General Remarks.

A copy of each of the following listed documents is enclosed herewith.

L. PATENTS

Patent No.

Date of Issue

Patentee(s)

JP 07201358

04-08-1995

Sony Corp.

US 5,656,876	12-08-1997	Radley et al.
EP 0653798	17-05-1995	Philips Electronics
US 5,912,092	15-06-1999	Hideaki et al.
JP 09139235	27-05-1997	Japan Storage Battery Co.
JP 11026029	29-01-1999	Sanyo Electric Co.
EP 0849815	24-06-1998	Matsushita Electric
US 4,861,685	29-08-1989	Matheny et al.
JP 10040885	13-02-1998	Matsushita Electric
WO 9203852	05-03-1992	Kodak Ltd.
EP 0644642	22-03-1995	Texas Instruments
JP 09266015	07-10-1997	Matsushita Electric

II. GENERAL REMARKS

It is respectfully requested that the above-listed patents and publications be given consideration by the Examiner and made of record in the subject patent application.

The information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Statement.

Respectfully submitted,

WELSH & KATZ, LTD.

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February 26, 2002 WELSH & KATZ, LTD. 120 South Riverside Plaza, 22nd Floor Chicago, Illinois 60606